



COLLÈGE
DE FRANCE
—1530—



Chaire de Physique Mésoscopique
Michel Devoret
Année 2010, 11 mai - 22 juin

INTRODUCTION AU CALCUL QUANTIQUE
INTRODUCTION TO QUANTUM COMPUTATION

Cinquième Leçon / *Fifth Lecture*

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10-V-1

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<http://www.physinfo.fr/lectures.html>

[PDF FILES OF ALL PAST LECTURES ARE POSTED](#)

Questions, comments and corrections are welcome!

write to "phymeso@gmail.com"

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CONTENT OF THIS YEAR'S LECTURES

QUANTUM COMPUTATION FROM THE PERSPECTIVE OF MESOSCOPIC CIRCUITS

1. Introduction, c-bits versus q-bits
2. The Pauli matrices and quantum computation primitives
3. Stabilizer formalism for state representation
4. Clifford calculus
5. Algorithms
6. Error correction

10-V-3

CALENDAR OF SEMINARS

May 11: Cristian Urbina, (Quantronics group, SPEC-CEA Saclay)

Josephson effect in atomic contacts and carbon nanotubes

May 18: Benoît Douçot (LPTHE / Université Pierre et Marie Curie)

Towards the physical realization of topologically protected qubits

June 1: Takis Kontos (LPA / Ecole Normale Supérieure)

Points quantiques et ferromagnétisme

June 8: Cristiano Ciuti (MPQ, Université Paris - Diderot)

Ultrastrong coupling circuit QED : vacuum degeneracy and quantum phase transitions

June 15: Leo DiCarlo (Yale)

Preparation and measurement of tri-partite entanglement in a superconducting quantum circuit

June 22: Vladimir Manucharian (Yale)

The fluxonium circuit: an electrical dual of the Cooper-pair box?

10-V-4

LECTURE IV : ALGORITHMS

Processing information with sequences of controlled reversible physical processes in a circuit.

1. Review of Clifford operations & logical circuits
2. The C-NOT and C-Phase gate
3. Preparing the GHZ state
4. Teleportation

10-V-5

OUTLINE

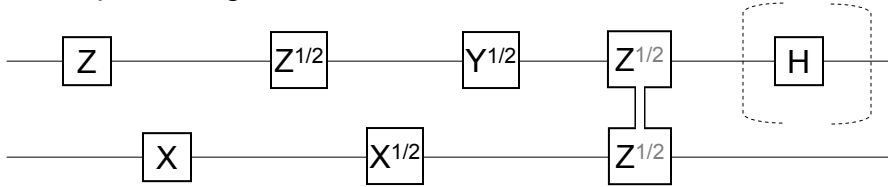
1. Review of Clifford operations & logical circuits
2. The C-NOT and C-Phase gates
3. Preparing the GHZ state
4. Teleportation

10-V-5a

CLIFFORD PRIMITIVES

They are 1- and 2-qubit gates that can simply be implemented by physical circuits and signal protocols.

Examples of logical circuit:



Corresponding super-operators for algebraic calculations:

$$[[ZI]] \quad [[IX]] \quad [[ZI]]^{1/2} \quad [[IX]]^{1/2} \quad [[YI]]^{1/2} \quad [[ZZ]]^{1/2} \quad ([[ZI]]^{1/2} [[XI]]^{1/2} [[ZI]]^{1/2})$$

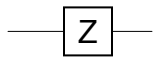
For 2-qubits these gates generate a group with 11520 elements!

n	1	2	3	4	5
C_n	24	11520	92897280	12128668876800	25410822678459187200

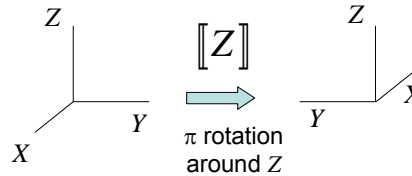
10-V-6b

1-QUBIT π -ROTATIONS

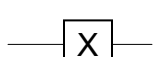
Phase flip
or "Z gate"



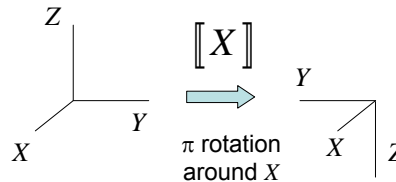
[[Z]]	
Z	Z
X	-X



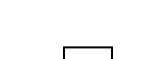
NOT, bit flip
or "X gate"



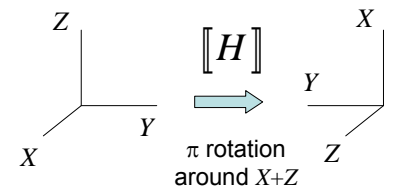
[[X]]	
Z	-Z
X	X



Hadamard

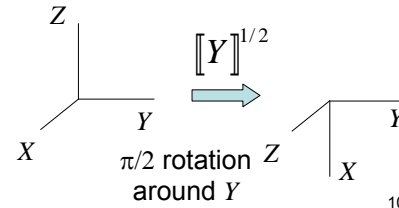
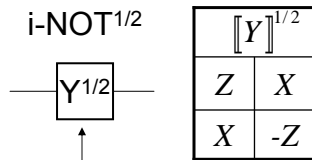
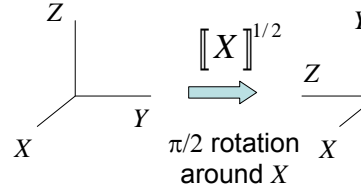
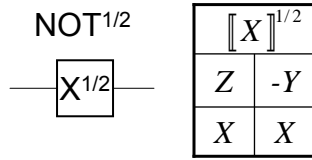
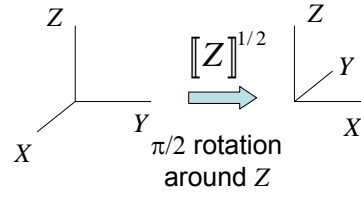
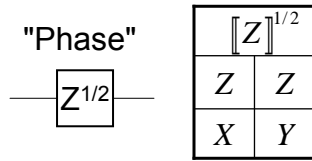


[[H]]	
Z	X
X	Z



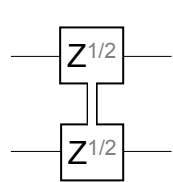
10-V-7

1-QUBIT $\pi/2$ -ROTATIONS



10-V-8

THE EFFICIENT 2-QUBIT PRIMITIVE



"Ising gate"

$[[ZZ]]^{1/2}$	
IZ	IZ
ZI	ZI
IX	ZY
XI	YZ

Generated by
secular interaction

$$\hat{H}_{\text{int}} = g_{\parallel} \sigma_z^1 \sigma_z^2$$

duration $\tau_s = \frac{\pi \hbar}{4g_{\parallel}}$

10-V-9

THE EFFICIENT 2-QUBIT PRIMITIVE

$[[ZZ]]^{1/2}$	
IZ	IZ
ZI	ZI
IX	ZY
XI	YZ

↑
can generate entanglement from product state

$[[ZI]]^{1/2} [[IZ]]^{1/2}$	
IZ	IZ
ZI	ZI
IX	IY
XI	YI

↑
transforms product state into product state

10-V-9a

THE EFFICIENT 2-QUBIT PRIMITIVE

"Ising gate"

$[[ZZ]]^{1/2}$	
IZ	IZ
ZI	ZI
IX	ZY
XI	YZ

Generated by secular interaction

$$\hat{H}_{\text{int}} = g_{\parallel} \sigma_z^1 \sigma_z^2$$

duration $\tau_s = \frac{\pi \hbar}{4g_{\parallel}}$

$[[ZZ]]^{1/2} IY = -ZX$ $[[ZZ]]^{1/2} YI = -XZ$ $[[ZZ]]^{1/2} XX = XX$ $[[ZZ]]^{1/2} YY = YY$	<p style="text-align: center;">vector product rule</p> <div style="display: flex; align-items: center; justify-content: center;"> $\hat{z} \times \hat{x} = \hat{y}$ </div> <p style="text-align: center;">supplemented by...</p>
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10-V-9b

RULES OF CLIFFORD CALCULUS FOR N QUBITS

They are found starting from: $[[B]]^\alpha A = [B]^{-\alpha} A [B]^\alpha$

$$\begin{aligned} [[B]]^1 [A] &= [-A] && \text{if } A \text{ and } B \text{ anticommute} \\ &= [A] && \text{if } A \text{ and } B \text{ commute} \end{aligned}$$

$$\begin{aligned} [[B]]^{1/2} [A] &= [B][A] && \text{if } A \text{ and } B \text{ anticommute} \\ &= [A] && \text{if } A \text{ and } B \text{ commute} \end{aligned}$$

Example: $[[Z]]^{1/2} X = Y$

Note that : $[-B]^\alpha = [[B]]^{-\alpha}$
 $[A] = [B] \Leftrightarrow A = B$

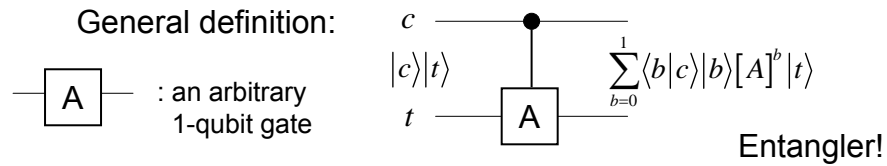
10-V-10a

OUTLINE

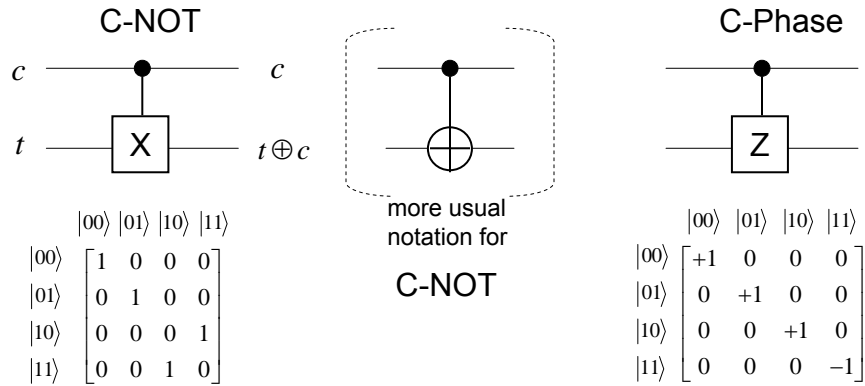
1. Review of Clifford operations & logical circuits
2. The C-NOT and C-Phase gates
3. Preparing the GHZ state
4. Teleportation

10-V-5b

CONTROLLED UNITARY



Two simple examples in the Clifford group:



10-V-11b

EXPRESSING CONDITIONALITY

Take $[A]^\beta = e^{-i\beta\frac{\pi}{2}A}$ where A is a multi-qubit Pauli

and extend β to a multi-qubit Pauli operator B , then

$$[A]^B = e^{-i\frac{\pi}{2}AB} = [AB] = [B]^A$$

when A and B are Pauli's and commute

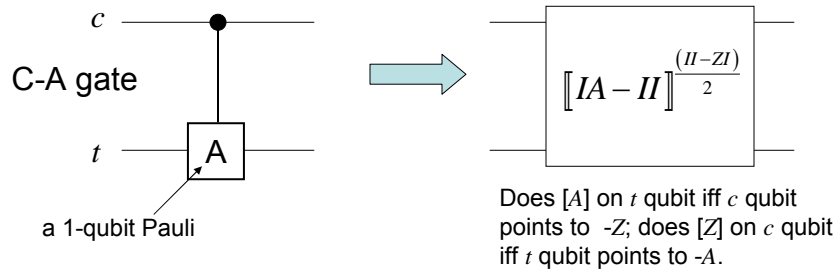
Doing B conditioned on the value of A is obtained by doing AB ?

Yes, but then, A is conditioned on the value of B !

Action-reaction principle constrains information processing

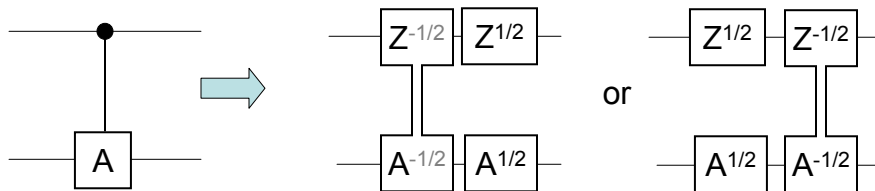
10-V-12a

EXPRESSION OF CONTROLLED UNITARY



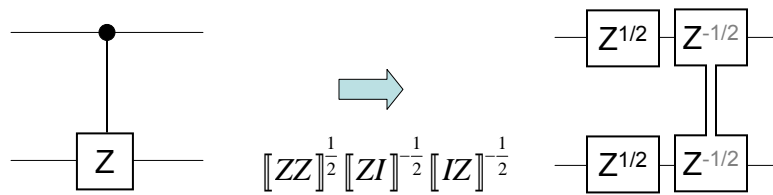
Using $[P]^Q = [QP]$ when P and Q are Paulis and commute, we get:

$$[C-A] = [ZA]^{-1/2} [ZI]^{+1/2} [IA]^{+1/2}$$



10-V-13a

THE C-PHASE GATE



C-Phase	
IZ	IZ
ZI	ZI
IX	ZX
XI	XZ

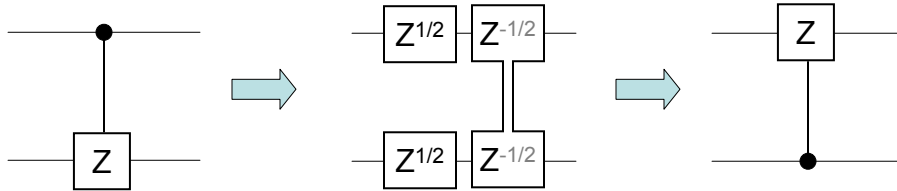
Check: $\hat{H}_{\text{int}} = g_{\parallel} (\sigma_z^1 \sigma_z^2 - \sigma_z^1 - \sigma_z^2)$
 $= g_{\parallel} (\sigma_z^1 - 1)(\sigma_z^2 - 1) + \text{const.}$

$$\hat{U}(\tau) = \exp(-i\hat{H}_{\text{int}}\tau/\hbar) \quad \tau_s = \frac{\pi\hbar}{4g_{\parallel}}$$

$$\hat{U}(\tau_s) = \begin{bmatrix} +1 & 0 & 0 & 0 \\ 0 & +1 & 0 & 0 \\ 0 & 0 & +1 & 0 \\ 0 & 0 & 0 & -1 \end{bmatrix} \quad \begin{array}{l} \text{up to} \\ \text{an overall} \\ \text{constant} \\ \text{phase} \\ \text{factor} \end{array}$$

10-V-14a

QUANTUM MECHANICS IMPOSES A FORM OF SYMMETRY BETWEEN CONTROL AND TARGET



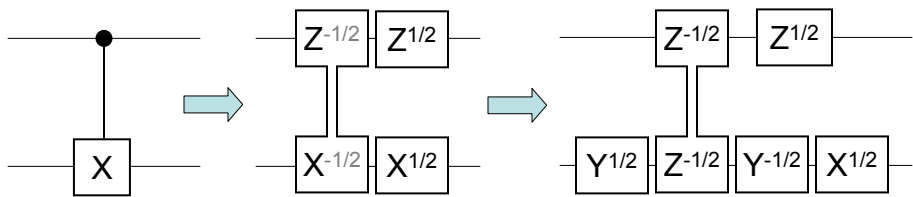
C-Phase	
IZ	IZ
ZI	ZI
IX	ZY
XI	YZ

This back-action of the target qubit on the control bit is fully general



10-V-15

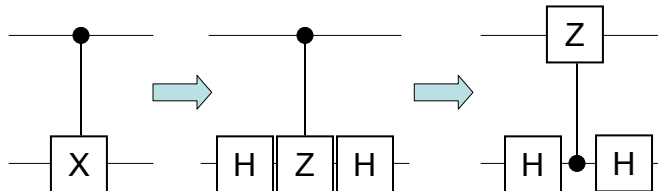
THE C-NOT GATE



$$[ZI]^{1/2} [IX]^{1/2} [ZX]^{-1/2} \rightarrow [ZI]^{1/2} [IX]^{1/2} [IY]^{-1/2} [ZZ]^{-1/2} [IY]^{+1/2}$$

C-NOT	
IZ	ZZ
ZI	ZI
IX	IX
XI	XX

other useful equivalences displaying c-t symmetry:



phase kick-back

10-V-16a

OUTLINE

1. Review of Clifford operations & logical circuits
2. The C-NOT and C-Phase gates
3. Preparing the Greenberger-Horne-Zeilinger state
4. Teleportation

10-V-5c

THE GHZ STATE

Greenberger-Horne-Zeilinger (1989) arXiv:0712.0921

**MAXIMAL THREE-PARTITE ENTANGLEMENT
NECESSARY FOR QUANTUM ERROR-CORRECTION**

$$|\text{GHZ}\rangle = \frac{|000\rangle + |111\rangle}{\sqrt{2}} \begin{array}{|l} +IZZ \\ +ZIZ \\ +XXX \end{array} \quad \frac{|000\rangle - |111\rangle}{\sqrt{2}} \begin{array}{|l} +IZZ \\ +ZIZ \\ -XXX \end{array}$$

$$XXX |000\rangle = |111\rangle$$

$$XXX |111\rangle = |000\rangle$$

10-V-17

CHARACTERISTIC PROPERTY OF GHZ: VIOLATION OF LOCAL HIDDEN VARIABLES THY's

Full stabilizer: $\{+IZZ, +ZIZ, +ZZI, +XXX, -XYY, -YXY, -YYX\}$

MEASURE THESE OPERATORS

CAN BE DONE ON THE SAME STATE SINCE THEY COMMUTE!

THEY INDIVIDUALLY YIELD RESULT +1
AND THUS THEIR PRODUCT IS +1

YET, "CLASSICALLY" THEIR PRODUCT SHOULD BE -1!

Multiply the
4 "words" as
if the "letters"
were either I
or $-I$

	$(+XXX)$
\times	$(-XYY)$
\times	$(-YXY)$
\times	$(-YYX)$
	$(-III)$

$$X^2 = Y^2 = Z^2 = I$$

Every symbol X and Y appears
twice in each of the 3 columns!
A contradiction between common
sense and experiment?

10-V-18a

GENERATING A GHZ-EQUIVALENT STATE

1) apply first rotations around x on each qubit to go to the transverse basis

$$\begin{Bmatrix} IIZ \\ IZI \\ ZII \end{Bmatrix} \xrightarrow{[XII]^{1/2} [IXI]^{1/2} [IIX]^{1/2}} \begin{Bmatrix} ILY \\ IYI \\ YII \end{Bmatrix}$$

2) apply "secular" IZZ interaction during " $\pi/2$ " amount of time

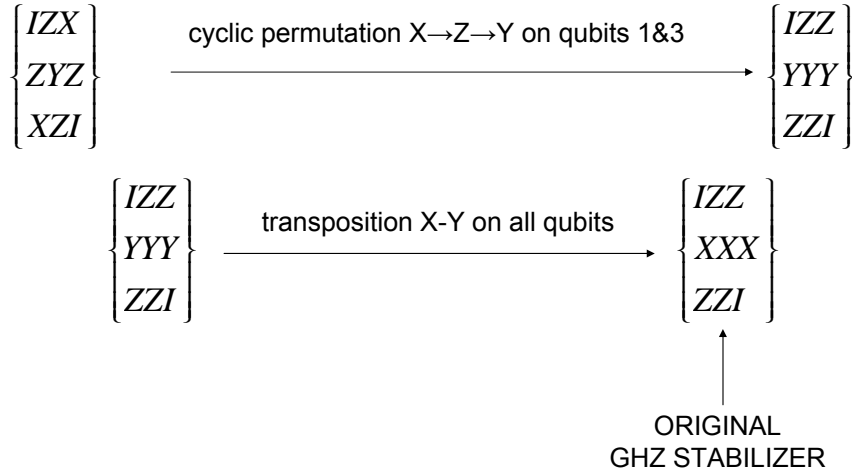
$$\begin{Bmatrix} ILY \\ IYI \\ YII \end{Bmatrix} \xrightarrow{[IZZ]^{1/2}} \begin{Bmatrix} IZX \\ IXZ \\ YII \end{Bmatrix}$$

3) apply "secular" ZZI interaction during " $\pi/2$ " amount of time

$$\begin{Bmatrix} IZX \\ IXZ \\ YII \end{Bmatrix} \xrightarrow{[ZZI]^{1/2}} \begin{Bmatrix} IZX \\ ZYZ \\ XZI \end{Bmatrix}$$

10-V-19b

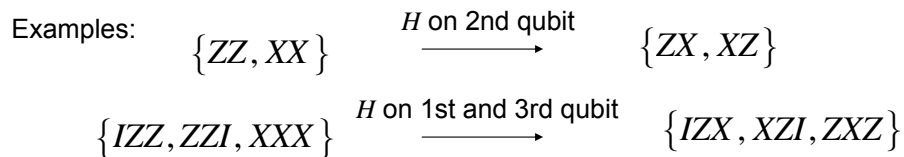
GHZ-EQUIVALENCE



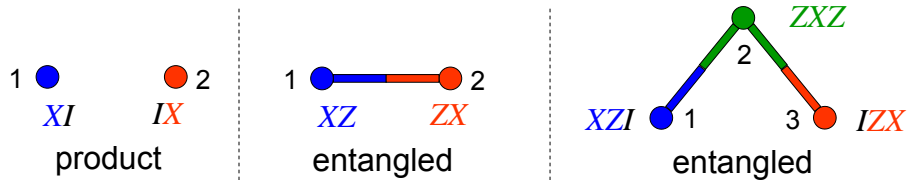
10-V-20

GRAPH STATES

Transform stabilizer generators to “standard form” through 1-qubit (local) Clifford group operations. Resulting stabilizer has an associated graph.



X symbol in operator (unique by construction) drawn as corresponding vertex.
Z symbol in operator drawn as half-edge in direction of corresponding vertex.
I symbol of the operator not drawn at all.



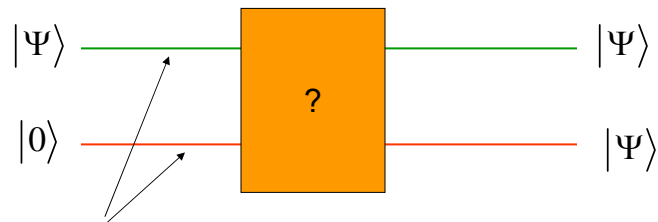
10-V-21

OUTLINE

1. Review of Clifford operations & logical circuits
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10-V-5d

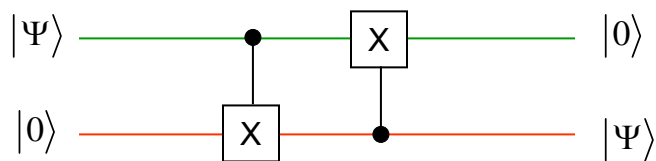
NO-CLONING THEOREM



Two different types of qubits
(ex.: nuclear spin and photon)

Impossible,
if $|\Psi\rangle$ is unknown

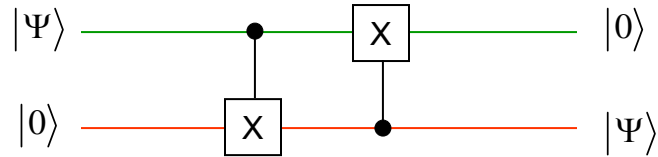
The only possible transfer is:



10-V-22a

THE TELEPORTATION PROBLEM

Consider the case where the basic solution to the quantum information transfer,

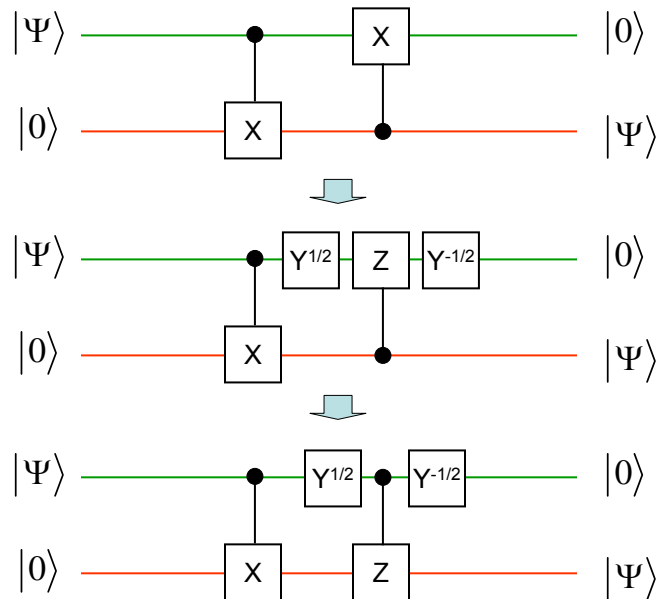


is not possible because there is no interaction possible between the green and red qubits. Also, the red qubit is not available for any two-bit quantum gate at the time of the transfer.

Can we still do the work?

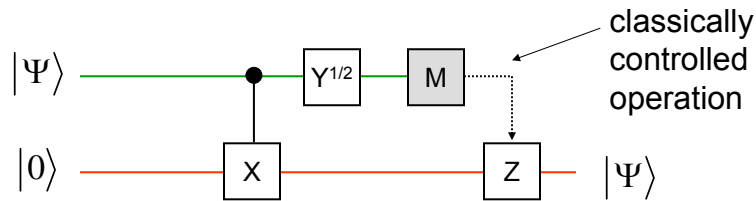
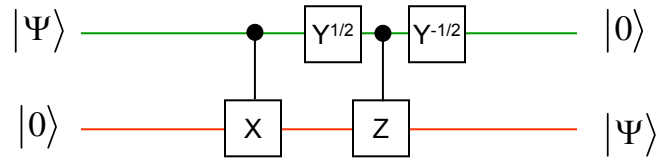
10-V-23

TELEPORTATION SOLUTION: 2nd C-NOT



10-V-24

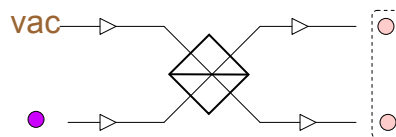
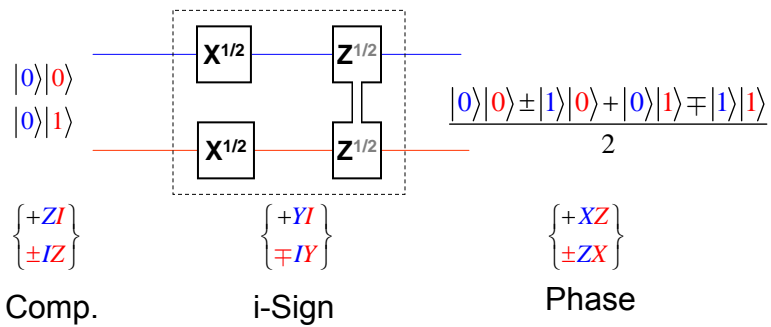
TELEPORTATION SOLUTION: 2nd C-NOT



Need now to transform the first C-NOT!

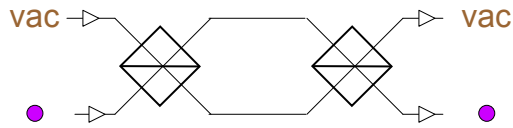
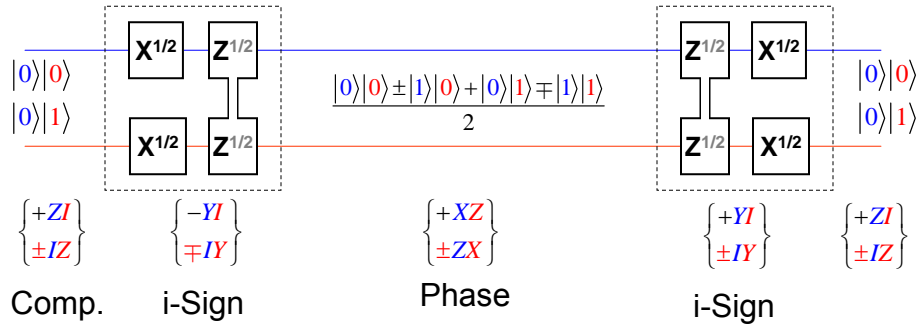
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THE "BEAM SPLITTER" GATE



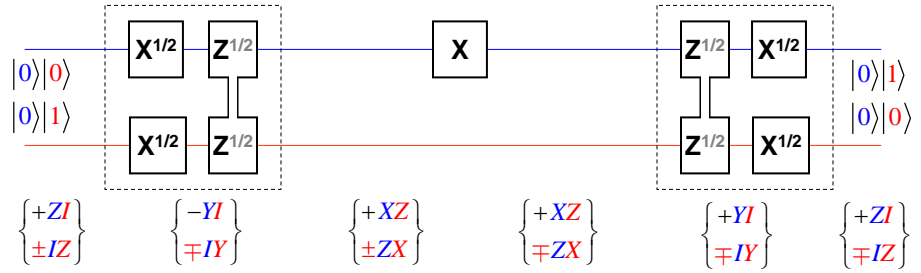
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QUBIT INTERFEROMETER

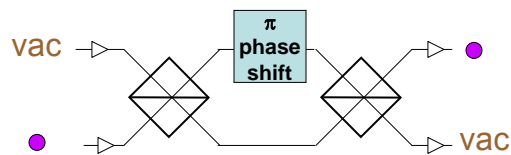


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QUBIT INTERFEROMETER

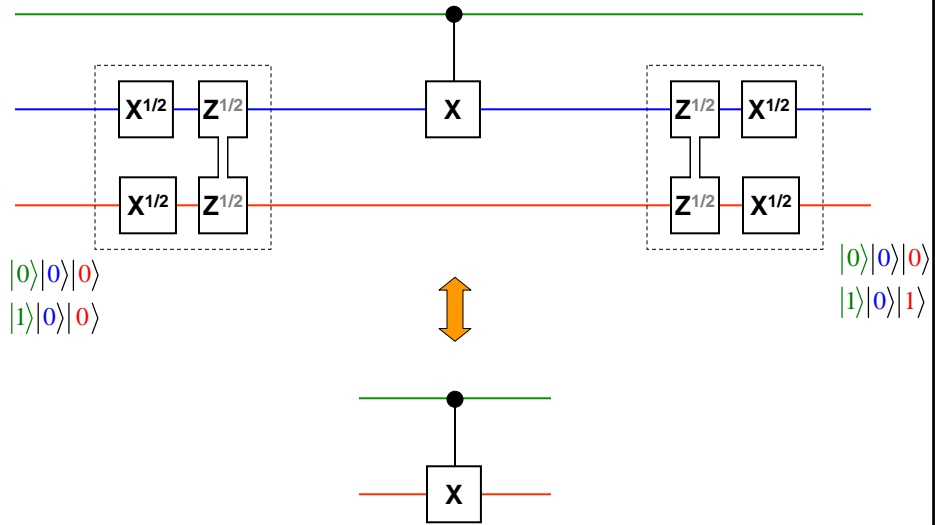


X flips **red qubit** outside by acting on **blue qubit** inside



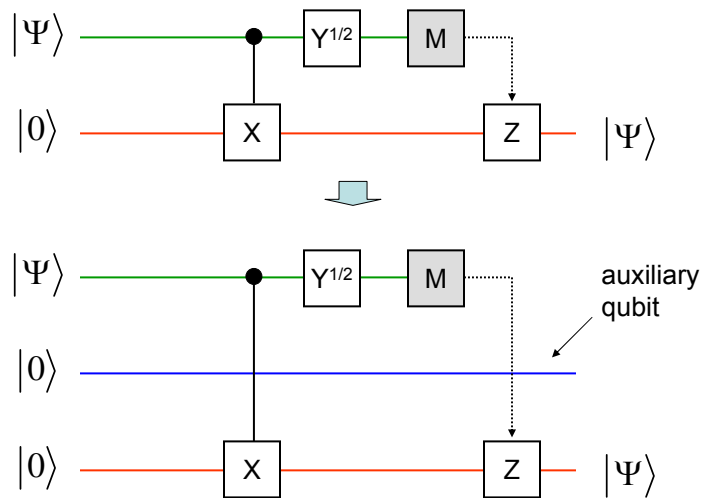
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REMOTE CONTROLLED NOT



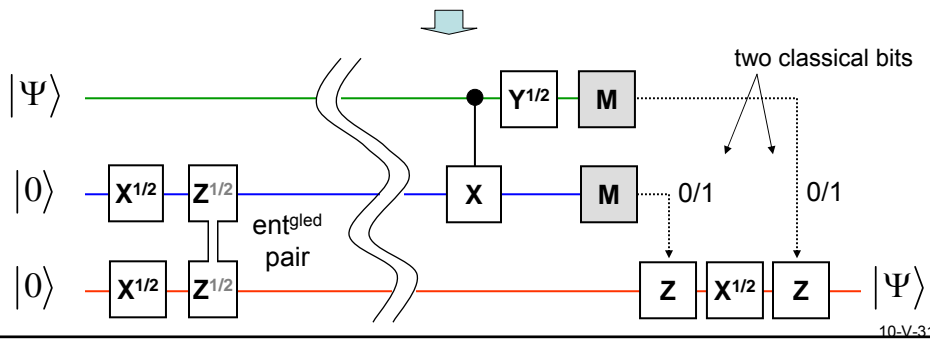
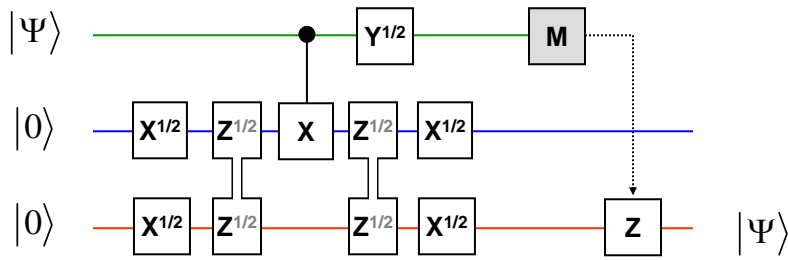
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TELEPORTATION SOLUTION: 1st C-NOT



10-V-30

TELEPORTATION SOLUTION: 1st C-NOT



END OF LECTURE